# Work-in-Progress: Towards a fine-grain thermal model for uniform multi-core processors



**CISTER** – Research Centre in **Real-Time & Embedded Computing Systems** 

### Motivation

#### Rao et al. model

#### Thermal models

- + Has very low complexity
- Neglects the heat transfer between neighboring cores

#### Matrix model

- + Has low complexity
- Models only steady state temperatures

#### HotSpot model

- + Is a fine-grain model
- Has very high complexity
- Models a high number of thermal layers
- Requires detailed information of the platform

## Goal

To design an efficient and simple thermal model for multi-core platforms to be coupled with a large variety of existing schedulers. This model must exhibit both transient and steady temperatures at run-time.

# **Concluding Remarks**

We provided a set of parameters; properties and a simple architectural/functional description of the hardware and software used to model the application and the platform.

## **Next Step**

To design efficient thermal-aware task-to-core mapping and scheduling strategies together with the associated analyses to reduce the average platform temperature.



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