



CISTER
Research Centre in
Real-Time & Embedded
Computing Systems

Technical Report

IPDeN 2.0: Real-time NoC with selective flit deflection and buffering (Appendix)

Yilian Ribot*

Geoffrey Nelissen

Eduardo Tovar*

*CISTER Research Centre

CISTER-TR-220906

2022

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Yilian Ribot*, Geoffrey Nelissen, Eduardo Tovar*

*CISTER Research Centre

Polytechnic Institute of Porto (ISEP P.Porto)

Rua Dr. António Bernardino de Almeida, 431

4200-072 Porto

Portugal

Tel.: +351.22.8340509, Fax: +351.22.8321159

E-mail: ribot@isep.ipp.pt, gnn@isep.ipp.pt, emt@isep.ipp.pt

<https://www.cister-labs.pt>

Abstract

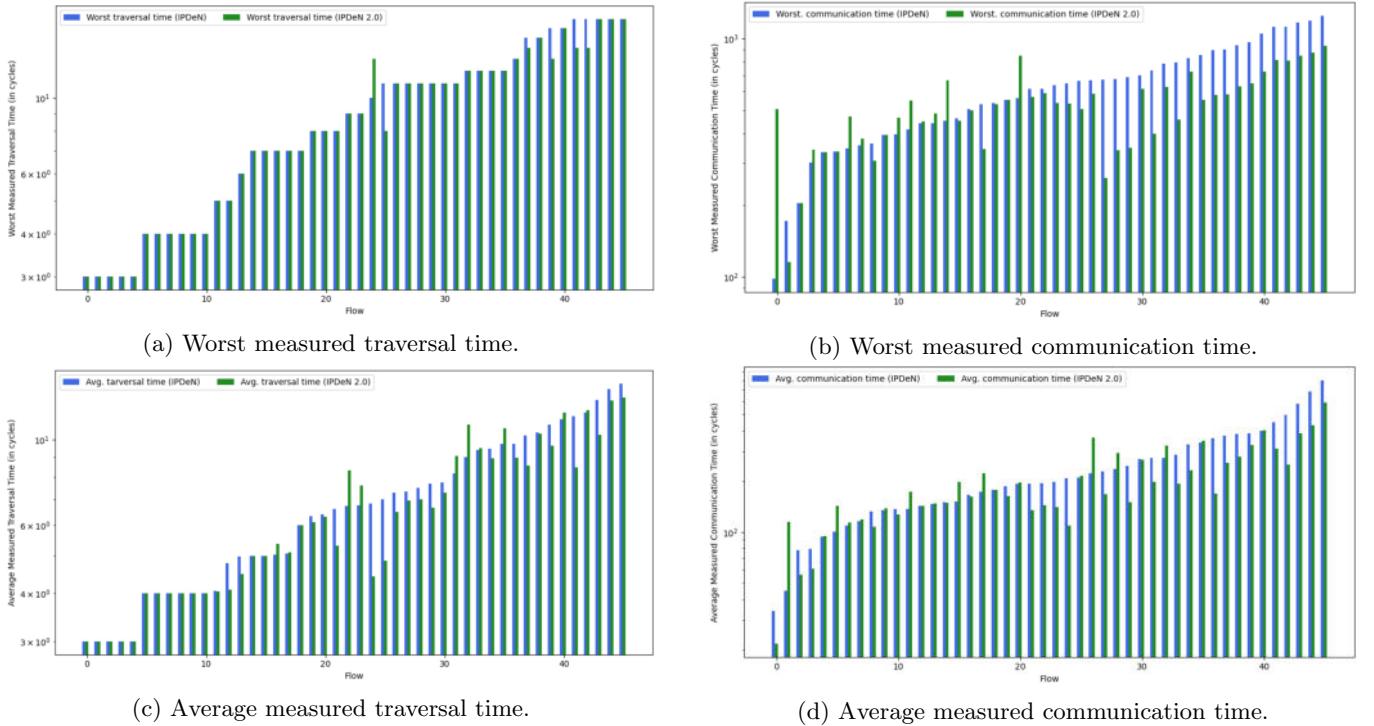


Figure 1: IPDeN 2.0 vs IPDeN (Synthetic test case).

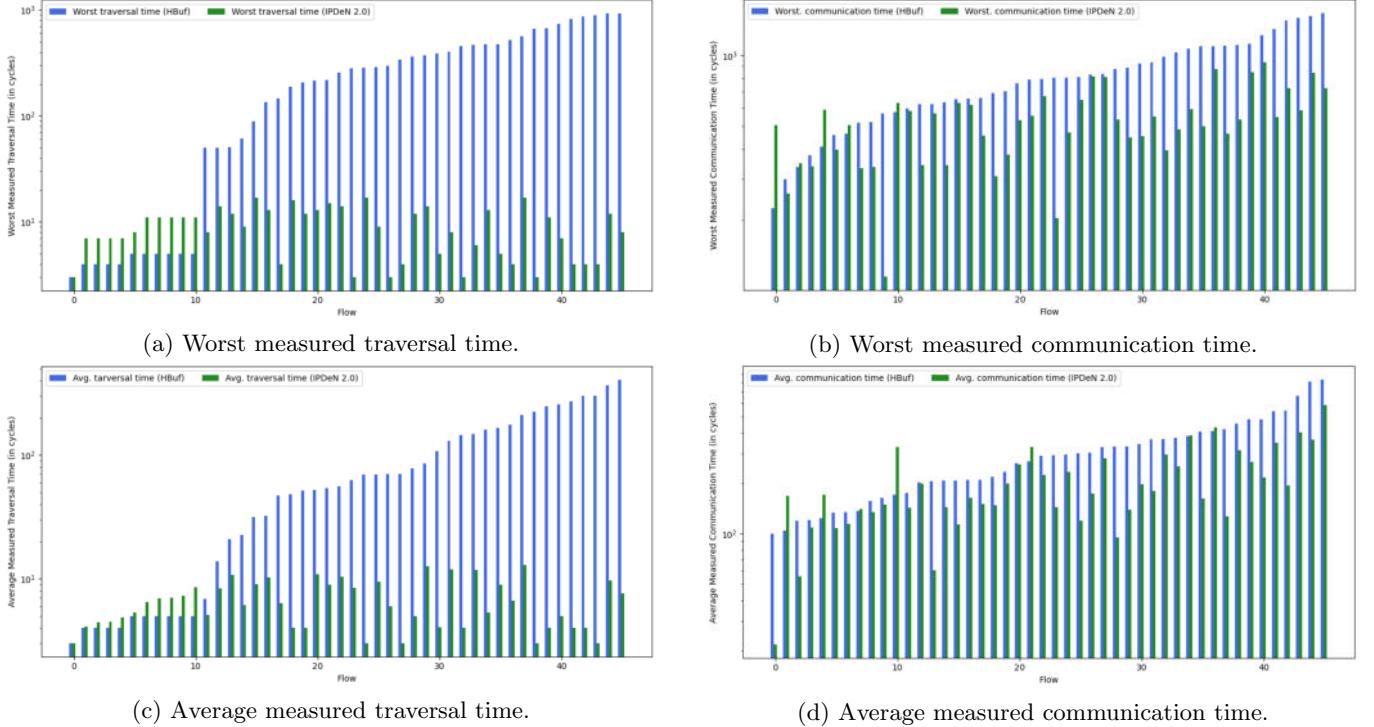


Figure 2: IPDeN 2.0 vs 128-deep HopliteBuf (Synthetic test case).

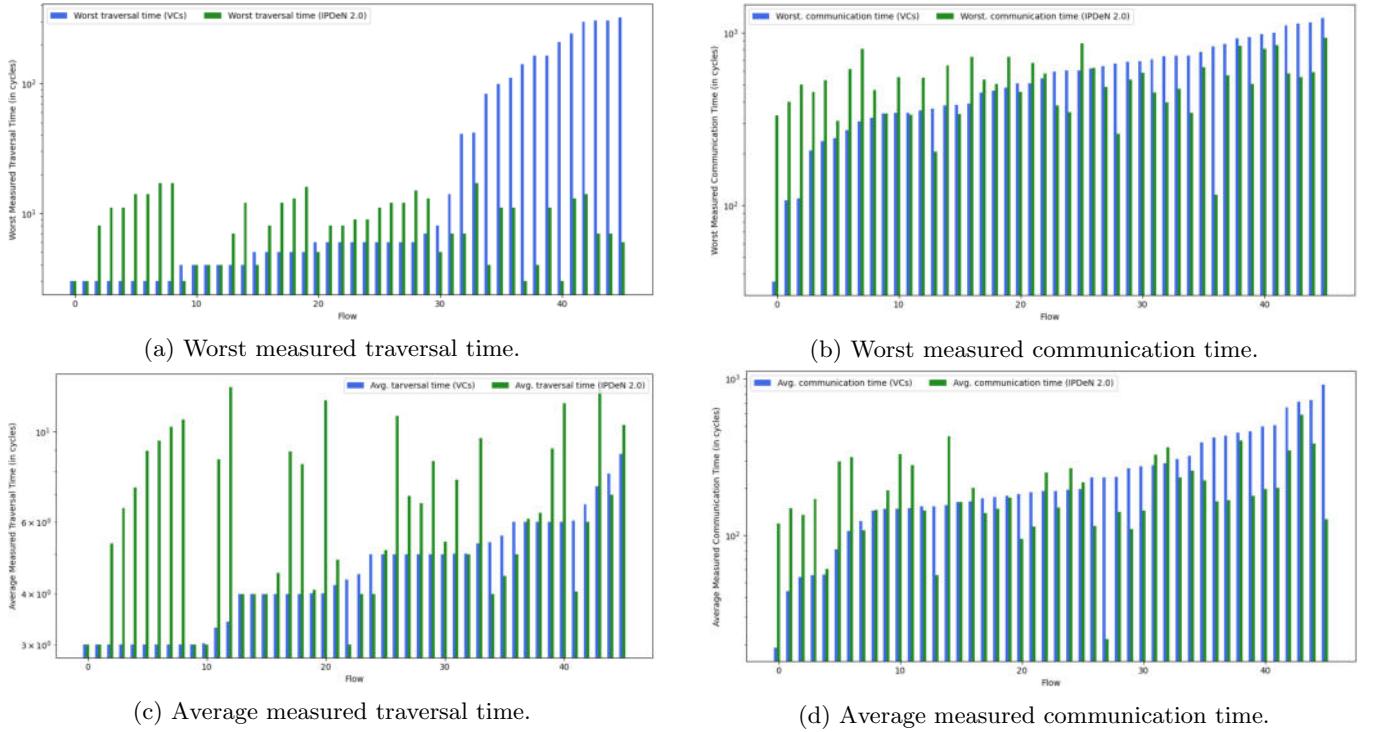


Figure 3: IPDeN 2.0 vs VCs (Synthetic test case).